

# Form C: Type Test Verification Report

All Micro-generators connected to the **DNO Distribution Network** shall be **Fully Type Tested**. This form is the **Manufacturer**'s declaration of compliance with the requirements of G98.

This form should be used when making a Type Test submission to the Energy Networks Association (ENA).

If the **Micro-generator** is **Fully Type Tested** and already registered with the ENA **Type Test Verification Report** Register, the **Installation Document** should include the **Manufacturer**'s Reference Number (the Product ID), and this form does not need to be submitted.

Where the **Micro-generator** is **Fully Type Tested** and not registered with the ENA **Type Test Verification Report** Register this form needs to be completed and provided to the **DNO**, to confirm that the **Microgenerator** has been tested to satisfy the requirements of this EREC G98.

Manufacturer's reference number		FOXES/03135/V1				
Micro-generator technology		Grid-tied photovoltaic inverter				
Manufacturer name		FOXESS	Co., Ltd.			
Address			Airport I	Room A203, Building C, No 205, Binghai Six Road, New Airport Industry Area, Longwan District, Wenzhou, Zhejiang Province		
Tel	0510-6809	92998		Fax		
E-mail	info@fox-	ess.com		Web site	www.fox-ess.com	
		Connection (	Option			
Registered use separate		3.0	kW single phase			
more than one connection option.		kW single phase				
		kW two phases in three phase system				
			kW two phases split phase system			

**Manufacturer Type Test** declaration. - I certify that all products supplied by the company with the above **Fully Type Tested** reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site modifications are required to ensure that the product meets all the requirements of EREC G98.

Signed	e. Wu	On behalf of	FOXESS Co., Ltd.
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Note that testing can be done by the **Manufacturer** of an individual component or by an external test house.

Where parts of the testing are carried out by persons or organisations other than the **Manufacturer** then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.



Operating Range: This test should be carried out as specified in EN 50438 D.3.1.

**Active Power** shall be recorded every second. The tests will verify that the **Micro-generator** can operate within the required ranges for the specified period of time.

The Interface Protection shall be disabled during the tests.

In case of a PV Micro-generator the PV primary source may be replaced by a DC source.

In case of a full converter **Micro-generator** (eg wind) the primary source and the prime mover **Inverter**/rectifier may be replaced by a **DC** source.

In case of a DFIG Micro-generator the mechanical drive system may be replaced by a test bench motor.

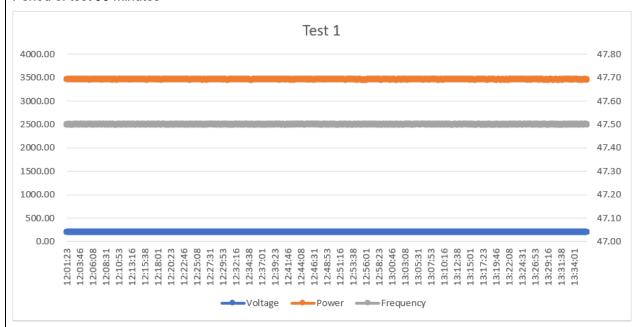
#### Test 1

Voltage = 85% of nominal (195.5 V)

Frequency = 47.5 Hz

Power factor = 1

Period of test 90 minutes



Test 2

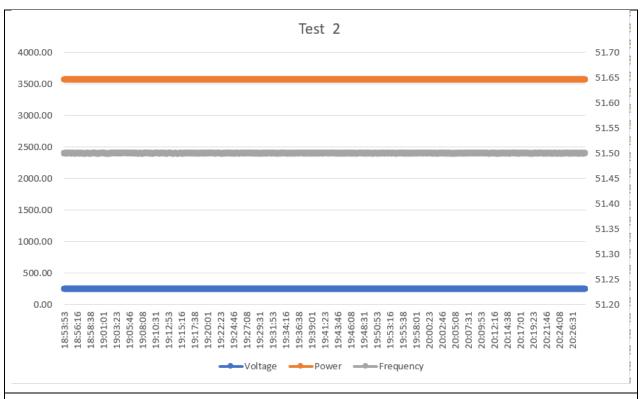
Voltage = 110% of nominal (253 V).

Frequency = 51.5 Hz

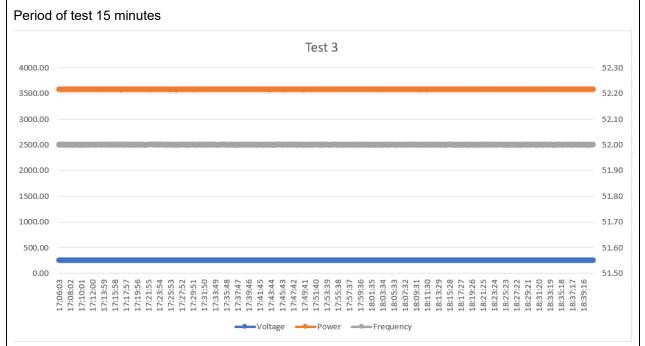
Power factor = 1

Period of test 90 minutes





Test 3
Voltage = 110% of nominal (253 V).
Frequency = 52.0 Hz
Power factor = 1





**Power Quality – Harmonics**: These tests should be carried out as specified in BS EN 61000-3-2. The chosen test should be undertaken with a fixed source of energy at two power levels a) between 45 and 55% and b) at 100% of **Registered Capacity**. The test requirements are specified in Annex A1 A.1.3.1 (**Inverter** connected) or Annex A2 A.2.3.1 (Synchronous).

#### Micro-generator tested to BS EN 61000-3-2 Micro-generator rating per phase 3.6 kW (rpp) Harmonic At 45-55% of **Registered** 100% of **Registered** Capacity Capacity Measured Value Measured Limit in Higher limit for odd MV in Amps Value MV in BS EN harmonics 21 and above Amps 61000-3-2 in Amps 2 0.031 0.035 1.080 3 0.132 0.185 2.300 4 0.012 0.013 0.430 5 0.078 0.090 1.140 6 0.010 0.012 0.300 7 0.058 0.064 0.770 8 0.011 0.011 0.230 9 0.047 0.048 0.400 10 0.011 0.011 0.184 11 0.040 0.042 0.330 12 0.011 0.012 0.153 13 0.036 0.035 0.210 14 0.010 0.012 0.131 15 0.030 0.035 0.150 16 0.010 0.010 0.115 17 0.024 0.030 0.132 18 0.011 0.011 0.102 19 0.023 0.027 0.118



	Measured Value MV in Amps		Measured Value M\ Amps			Limit in BS EN 61000- 3-2 in Amps	Higher limit for odd harmonics 21 and above
Harmonic	At 45-55% of Rec Capacity		100% of Registered Capacity				I
Micro-ge	enerator rating per (rpp)	phase	3.0	ŀ	(W		
40	0.009		0.009			0.046	
39	0.009		0.009			0.058	0.087
38	0.008		0.008			0.048	
37	0.008		0.009			0.061	0.091
36	0.008		0.009			0.051	
35	0.008		0.011			0.064	0.096
34	0.009		0.009			0.054	
33	0.010		0.013			0.068	0.102
32	0.010		0.009			0.058	
31	0.009		0.015			0.073	0.109
30	0.010		0.009			0.061	
29	0.012		0.016			0.078	0.117
28	0.010		0.010			0.066	
27	0.014		0.018			0.083	0.124
26	0.011		0.012			0.071	
25	0.022		0.026			0.090	0.135
24	0.010		0.011			0.077	
23	0.016		0.023			0.098	0.147
22	0.011		0.011			0.084	
21	0.018		0.026			0.107	0.160
20	0.010		0.013			0.092	



2	0.029	0.029	1.080	
3	0.131	0.160	2.300	
4	0.013	0.015	0.430	
5	0.075	0.083	1.140	
6	0.011	0.011	0.300	
7	0.057	0.061	0.770	
8	0.010	0.010	0.230	
9	0.048	0.046	0.400	
10	0.011	0.011	0.184	
11	0.039	0.042	0.330	
12	0.011	0.011	0.153	
13	0.032	0.035	0.210	
14	0.011	0.011	0.131	
15	0.029	0.032	0.150	
16	0.011	0.010	0.115	
17	0.021	0.031	0.132	
18	0.012	0.012	0.102	
19	0.021	0.027	0.118	
20	0.010	0.012	0.092	
21	0.016	0.024	0.107	0.160
22	0.010	0.010	0.084	
23	0.016	0.021	0.098	0.147
24	0.010	0.010	0.077	
25	0.020	0.026	0.090	0.135
26	0.012	0.011	 0.071	
27	0.013	0.017	0.083	0.124
28	0.009	0.010	 0.066	
29	0.011	0.014	0.078	0.117



30	0.009	0.009	0.061	
31	0.009	0.013	0.073	0.109
32	0.008	0.009	0.058	
33	0.010	0.012	0.068	0.102
34	0.009	0.008	0.054	
35	0.008	0.010	0.064	0.096
36	0.008	0.009	0.051	
37	0.008	0.009	0.061	0.091
38	0.009	0.009	0.048	
39	0.008	0.009	0.058	0.087
40	0.008	0.010	0.046	

Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.

**Power Quality – Voltage fluctuations and Flicker**: These tests should be undertaken in accordance with EREC G98 Annex A1 A.1.3.3 (**Inverter** connected) or Annex A2 A.2.3.3 (Synchronous).

	Starting			Stoppin	Stopping			Running		
	d max	d c	d(t)	d max	d c	d(t)		Pst		P <sub>lt</sub> 2 hours
Measured Values at test impedance	0	0	0	0	0	0		0.07		0.07
Normalised to standard impedance	0	0	0	0	0	0		0.07		0.07
Normalised to required maximum impedance	NA	NA	NA	NA	NA	NA		NA		NA
Limits set under BS EN 61000-3-11	4%	3.3%	3.3%	4%	3.3%	3.3%		1.0		0.65
Test Impedance	R	0.4		Ω	X		0.2	25	Ω	



Standard Impedance	R	0.24 * 0.4 ^	Ω	X	0.15 * 0.25 ^	Ω
Maximum Impedance	R		Ω	X		Ω

<sup>\*</sup>Applies to three phase and split single phase **Micro-generators**.

For voltage change and flicker measurements the following formula is to be used to convert the measured values to the normalised values where the power factor of the generation output is 0.98 or above.

Normalised value = Measured value\*reference source resistance/measured source resistance at test point.

Single phase units reference source resistance is 0.4  $\Omega$ 

Two phase units in a three phase system reference source resistance is  $0.4 \Omega$ .

Two phase units in a split phase system reference source resistance is 0.24  $\Omega$ .

Three phase units reference source resistance is 0.24  $\Omega$ .

Where the power factor of the output is under 0.98 then the X to R ratio of the test impedance should be close to that of the Standard Impedance.

The stopping test should be a trip from full load operation.

The duration of these tests need to conform to the particular requirements set out in the testing notes for the technology under test. Dates and location of the test need to be noted below.

Test start date	2020-01-19	Test end date	2020-01-19
Test location	DEKRA Testing Lab N 215006, Jiangsu, P.R.		ad, Suzhou Industrial Park , Suzhou,

### Power quality – DC injection: This test should be carried out in accordance with EN 50438 Annex D.3.10

•	<u> </u>									
	3.6 kW									
Test power level	20%	50%	75%	100%						
Recorded value in Amps	0.0070	0.0041	-0.0035	-0.0075						
as % of rated AC current	0.04%	0.03%	-0.02%	-0.05%						
Limit	0.25%	0.25%	0.25%	0.25%						
			3.0 kW							
Test power level	20%	50%	75%	100%						
Recorded value in Amps	0.0063	0.0050	-0.0021	-0.0046						
as % of rated AC current	0.05%	0.04%	-0.02%	-0.04%						

<sup>^</sup> Applies to single phase **Micro-generators** and **Micro-generators** using two phases on a three phase system.



Limit 0.25% 0.25% 0.25% 0.25%

**Power Quality – Power factor**: This test shall be carried out in accordance with EN 50538 Annex D.3.4.1 but with nominal voltage -6% and +10%. Voltage to be maintained within ±1.5% of the stated level during the test.

the test.										
	3.6 kW									
	216.2 V	230 V	253 V							
20% of Registered Capacity	0.984	0.984	0.982							
50% of Registered Capacity	0.996	0.996	0.996							
75% of Registered Capacity	0.998	0.998	0.998							
100% of Registered Capacity	0.999	0.999	0.999							
Limit	>0.95	>0.95	>0.95							
		3.0 kW								
	216.2 V	230 V	253 V							
20% of Registered Capacity	0.982	0.998	0.978							
50% of Registered Capacity	0.995	0.995	0.994							
75% of Registered Capacity	0.997	0.997	0.997							
100% of Registered Capacity	0.998	0.998	0.998							
Limit	>0.95	>0.95	>0.95							



**Protection – Frequency tests:** These tests should be carried out in accordance with EN 50438 Annex D.2.4 and the notes in EREC G98 Annex A1 A.1.2.3 (**Inverter** connected) or Annex A2 A.2.2.3 (Synchronous)

Function	Setting		Trip test		"No trip tests"		
	Frequency	Time delay	Frequency	Time delay	Frequency /time	Confirm no trip	
U/F stage 1	47.5 Hz	20 s	47.48 Hz	20.35s	47.7 Hz 30 s	No trip	
U/F stage 2	47 Hz	0.5 s	46.99 Hz	0.53s	47.2 Hz 19.5 s	No trip	
					46.8 Hz 0.45 s	No trip	
O/F stage 1	52 Hz	0.5 s	52.03Hz	0.54s	51.8 Hz 120.0 s	No trip	
					52.2 Hz 0.45 s	No trip	

Note. For frequency trip tests the frequency required to trip is the setting  $\pm$  0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No trip tests" need to be carried out at the setting  $\pm$  0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

**Protection – Voltage tests:** These tests should be carried out in accordance with EN 50438 Annex D.2.3 and the notes in EREC G98 Annex A1 A.1.2.2 (**Inverter** connected) or Annex A2 A.2.2.2 (Synchronous)

Function	Setting		Trip test		"No trip tests"		
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip	
U/V	184 V	2.5 s	183.7V	2.560s	188 V 5.0 s	No trip	
					180 V 2.45 s	No trip	
O/V stage 1	262.2 V	1.0 s	262.3V	1.045s	258.2 V 5.0 s	No trip	
O/V stage 2	273.7 V	0.5 s	273.8V	0.533s	269.7 V 0.95 s	No trip	
					277.7 V 0.45 s	No trip	

Note for Voltage tests the Voltage required to trip is the setting ±3.45 V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting ±4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.



**Protection – Loss of Mains test:** For PV **Inverters** shall be tested in accordance with BS EN 62116. Other **Inverters** should be tested in accordance with EN 50438 Annex D.2.5 at 10%, 55% and 100% of rated power.

For **Inverters** tested to BS EN 62116 the following sub set of tests should be recorded in the following table.

Test Power and imbalance	33%	66%	100%	33%	66%	100%
	-5% Q	-5% Q	-5% P	+5% Q	+5% Q	+5% P
	Test 22	Test 12	Test 5	Test 31	Test 21	Test 10
Trip time. Limit is 0.5 s	0.336 s	0.292 s	0.381 s	0.335 s	0.210 s	0.265 s

**Protection – Frequency change, Vector Shift Stability test:** This test should be carried out in accordance with EREC G98 Annex A1 A.1.2.6 (**Inverter** connected) or Annex A2 A.2.2.6 (Synchronous).

	Start Frequency	Change	Confirm no trip
Positive Vector Shift	49.0 Hz	+50 degrees	No trip
Negative Vector Shift	50.0 Hz	- 50 degrees	No trip

**Protection – Frequency change, RoCoF Stability test:** The requirement is specified in section 11.3, test procedure in Annex A.1.2.6 (**Inverter** connected) or Annex A2 A.2.2.6 (Synchronous).

Ramp range	Test frequency ramp:	Test Duration	Confirm no trip
49.0 Hz to 51.0 Hz	+0.95 Hzs <sup>-1</sup>	2.1 s	No trip
51.0 Hz to 49.0 Hz	-0.95 Hzs <sup>-1</sup>	2.1 s	No trip

**Limited Frequency Sensitive Mode – Overfrequency test:** This test should be carried out in accordance with EN 50438 Annex D.3.3 Power response to over- frequency. The test should be carried out using the specific threshold frequency of 50.4 Hz and **Droop** of 10%.

Test sequence at Registered Capacity =100%	Measured Active Power Output	Frequency	Primary Power Source	Active Power Gradient
Step a) 50.00 Hz ±0.01 Hz	3569.8	50.00		NA
Step b) 50.45 Hz ±0.05 Hz	3536.6	50.45		10.75%
Step c) 50.70 Hz ±0.10 Hz	3367.3	50.70	DC SOURCE	10.58%
Step d) 51.15 Hz ±0.05 Hz	3053.1	51.15		10.36%
Step e) 50.70 Hz ±0.10 Hz	3360.6	50.70		10.24%
Step f) 50.45 Hz ±0.05 Hz	3536.1	50.45		10.59%
Step g) 50.00 Hz ±0.01 Hz	3569.8	50.00		NA



Test sequence at Registered Capacity 50%	Measured Active Power Output	Frequency	Primary Power Source	Active Power Gradient
Step a) 50.00 Hz ±0.01 Hz	1830	50.00		NA
Step b) 50.45 Hz ±0.05 Hz	1823	50.45		10.82%
Step c) 50.70 Hz ±0.10 Hz	1735	50.70	DC SOURCE	10.51%
Step d) 51.15 Hz ±0.05 Hz	1580	51.15		10.62%
Step e) 50.70 Hz ±0.10 Hz	1733	50.70		10.32%
Step f) 50.45 Hz ±0.05 Hz	1821	50.45		9.68%
Step g) 50.00 Hz ±0.01 Hz	1830	50.00		NA

Steps as defined in EN 50438

**Power output with falling frequency test:** This test should be carried out in accordance with EN 50438 Annex D.3.2 active power feed-in at under-frequency.

Test sequence	Measured Active Power Output	Frequency	Primary power source
Test a) 50 Hz ± 0.01 Hz	3570.0 W	50.00 Hz	3678.9 W
Test b) Point between 49.5 Hz and 49.6 Hz	3573.4 W	49.55 Hz	3681.9 W
Test c) Point between 47.5 Hz and 47.6 Hz	3574.4 W	47.55 Hz	3684.4 W

NOTE: The operating point in Test (b) and (c) shall be maintained for at least 5 minutes

#### Re-connection timer.

Test should prove that the reconnection sequence starts after a minimum delay of 20 s for restoration of voltage and frequency to within the stage 1 settings of Table 2.

		1				
Time delay setting	Measured delay	Checks on no reconnection when voltage or frequency is brought to just outside stage 1 limits of table 2.				
60s	92s	At 266.2 V	At 180.0 V	At 47.4 Hz	At 52.1 Hz	
Confirmation Micro-generation not re-connection	rator does	No reconnection	No reconnection	No reconnection	No reconnection	

**Fault level contribution**: These tests shall be carried out in accordance with EREC G98 Annex A1 A.1.3.5 (**Inverter** connected) and Annex A2 A.2.3.4 (Synchronous).

For machines with electro-magnet	tic output		For <b>Inve</b>	erter o	utput	
Parameter	Symbol	Value	Time fault	after	Volts	Amps

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Peak Short Circuit current	ĺρ	20 ms	237.5V	12.6A
Initial Value of aperiodic current	Α	100 ms	212.5V	0A
Initial symmetrical short-circuit current*	<b>I</b> <sub>k</sub>	250 ms	150V	0A
Decaying (aperiodic) component of short circuit current*	İDC	500 ms	81.25V	0A
Reactance/Resistance Ratio of source*	X/ <sub>R</sub>	Time to trip	4.2ms	In seconds

For rotating machines and linear piston machines the test should produce a 0 s - 2 s plot of the short circuit current as seen at the **Micro-generator** terminals.

\* Values for these parameters should be provided where the short circuit duration is sufficiently long to enable interpolation of the plot

Logic Interface.	Yes
Self-Monitoring solid state switching: No specified test requirements. Refer to EREC G98 Annex A1 A.1.3.6 (Inverter connected).	NA
It has been verified that in the event of the solid state switching device failing to disconnect the <b>Micro-generator</b> , the voltage on the output side of the switching device is reduced to a value below 50 V within 0.5 s.	
Additional comments	